

ABSTRACT OF THE DISCLOSURE

A system and method is described for modulating the operating speed of a microprocessor, or other processing element, and the duration of chip select signals in response to the selected microprocessor operating speed. The microprocessor clock is modulated by division, multiplication, or alternative clock selection, as directed by instructions executing on the microprocessor. Power consumption by the microprocessor may be thereby reduced under program control in accord with the required level of processor activity. As the power consumed by typical peripheral devices is largely proportional to the activity of the chip select input at a given operating voltage, the present invention further describes circuits and methods of modulating the duration of the chip select outputs from a microprocessor responsive to the operating speed of the microprocessor to lower power dissipation levels within the associated peripheral devices.

DRAWING(S)

There are attached five (5) sheets of drawings.

EXECUTED OATH OR DECLARATION

An executed declaration shall follow.

SCANNED, # _____



SCANNED, # _____

SEQUENCE LISTING

Not Applicable

